



S/N 10/815,542

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Choong Kooi Chee	Examiner:	Chris Chu
Serial No.:	10/815,542	Group Art Unit:	2815
Filed:	March 31, 2004	Docket:	884.C20US1
Title:	STRESS-COMPENSATION LAYERS IN CONTACT ARRAYS, AND PROCESSES OF MAKING SAME		
Assignee:	Intel Corporation	Customer No:	21186

AMENDMENT AND RESPONSE UNDER 37 C.F.R. § 1.111

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicant has reviewed the Office Action mailed on June 27, 2005. Please amend the above-identified patent application as follows.

PRELIMINARY REMARKS

Applicant gratefully acknowledges the indication of allowability of claims 10-16 and 32-44. Applicant also gratefully acknowledges the indication of provisional allowability of claims 4, 9 and 31.